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## \*\* CONTINUING DATA \*\*\*\*\*

None S.W.S.

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2003-005766 01/14/2003

S.W.S.

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged <i>S.W.S.</i> Examiner's Signature Initials	JAPAN	6	10	1

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## TITLE

Method of manufacturing a semiconductor device with penetration electrodes that protrude from a rear side of a substrate formed by thinning the substrate

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